

151103778

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. 09/228,705
Priority Filing Date January 12, 1999
Inventor Joseph M. Brand
Assignee Micron Technology, Inc.
Priority Group Art Unit 2826
Priority Examiner Alexander O. Williams
Attorney's Docket No. MI22-1939
Title: Integrated Circuit Device

INFORMATION DISCLOSURE STATEMENT

References -- See attached Form PTO-1449

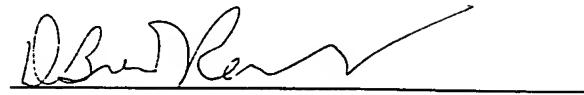
In compliance with 37 C.F.R. §§ 1.56, 1.97 and §1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a continuation application of co-pending Application Serial No. 09/228,705, filed January 12, 1999, upon which the above-identified application relies for a priority date under 35 U.S.C. §120. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. §1.98(d) and MPEP §609(2).

Citation of these references is respectfully requested.

Respectfully submitted,

Date: 2-5-02



D. Brent Kenady
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Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE TRADE		ATTY. DOCKET NO. MI22-1939		SERIAL NO. Priority: 09/228,705	
LIST OF ART CITED BY APPLICANT <small>(Use several sheets if necessary)</small>				APPLICANT Joseph M. Brand			
				FILING DATE Filed herewith		GROUP Priority: 2326	
U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	5,698,899	12/1997	Hirakawa et al.	257	712	
	AB	5,331,205	07/1994	Primeaux	257	790	
	AC	5,249,101	09/1993	Frey et al.	361	717	
	AD	5,379,186	01/1995	Gold et al.	361	706	
	AE	5,847,467	12/1998	Wills et al.	257	789	
	AF	5,352,851	10/1994	Wallace et al.	174	52,4	
	AG	5,846,175	01/1999	Burns	257	684	
	AH	6,144,107	11/2000	Narita	257	789	
	AI	5,057,457	10/1991	Miyahara et al.	437	207	
	AJ	5,946,556	08/1999	Hashizume	438	136	
	AK	5,789,813	08/1998	Kirkland et al.	257	712	
	AL	6,104,093	08/2000	Caletka et al.	257	778	
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes
	AM	JP 357133653A	08/1982	Japan (abstract translated)			x
	AN	JP 363141353A	06/1988	Japan (abstract translated)			x
	AO						
	AP						
	AQ						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
	AR		SLD4MI8DR400 4Meg x 18 SDRAM. www.sldram.com. pgs. 1-12				
	AS		How to make IC Package. www.msato@iis.u-tokyo.ac.jp - 10 pgs.				
	AT		"Principles of Electronic Packaging", Donald P. Seraphim, Ronald C. Lasky and Che-Yu Li. McGraw-Hill Book Company. pgs. 130, 131				
				and 600.			
EXAMINER				DATE CONSIDERED			
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

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Form PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				ATTY. DOCKET NO. MI22-1939		SERIAL NO. Priority: 09 228,705		
				APPLICANT Joseph M. Brand				
				FILING DATE Filed herewith		GROUP Priority: 2826		
								
*Examiner Initial		Document Number	Date	Name		Class	Subclass	Filing Date If Appropriate
	AA	5,406,117	04/1995	Dlugokecki		257	659	
	AB							
	AC							
	AD							
	AE							
	AF							
	AG							
	AH							
	AI							
	AJ							
	AK							
	AL							
FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
	AM						Yes	No
	AN							
	AO							
	AP							
	AQ							
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
	AR		"Handbook of Electronic Package Design", Michael Pecht, Marcel Dekker, Inc., pgs. 95-97 and 219-220.					
	AS		"The Authoritative Dictionary of IEEE Standards Terms", Seventh Edition, pg. 510					
	AT							
EXAMINER				DATE CONSIDERED				
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EV318282089

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/072,417
Filing Date February 5, 2002
Inventor Joseph M. Brand
Assignee Micron Technology, Inc.
Group Art Unit 2826
Examiner P.L. Greene
Attorney's Docket No. MI22-1939
Title: Integrated Circuit Device

STATEMENT OF COMMON OWNERSHIP

To: Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

From: D. Brent Kenady
Tel. 509-624-4276; Fax 509-838-3424
Wells St. John P.S.
601 West First Avenue, Suite 1300
Spokane, WA 99201-3828

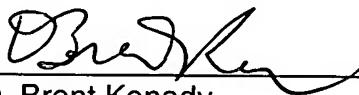
Sir,

The above-referenced application and the prior art presented in the Office Action mailed March 14, 2003 (Paper No. 7), Corisis et al. (U.S. Patent No. 6,326,687), were at the time the invention was made, owned by, or subject to, an obligation of assignment to the same organization. MPEP §706.02(l)(3) (8th ed.).

The undersigned is an attorney of record.

Respectfully submitted,

Dated: 8-14-03

By: 
D. Brent Kenady
Reg. No. 40,045

RECEIVED
TC 2800 MAIL ROOM
AUG 21 2003



FV31828208

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/072,417
Filing Date February 5, 2002
Inventor Joseph M. Brand
Assignee Micron Technology, Inc.
Group Art Unit 2826
Examiner P.L. Greene
Attorney's Docket No. MI22-1939
Title: Integrated Circuit Device

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AUG 21 2003
1C 2800 MAIL ROOM

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Reference -See Attached Form PTO-1449

The Examiner's attention is directed to the reference which is listed on the attached Form PTO-1449, a copy of which are attached. No admission is made regarding whether the submitted reference is prior art.

Citation of the referenced art is respectfully requested.

08/19/2003 DTESEM1 00000080 10072417

02 FC:1806

180.00 0P

Respectfully submitted,

Dated: 8-14-03

By: D. Brent Kenady
D. Brent Kenady
Reg. No. 40,045



Form PTO-1449

LIST OF ART CITED BY APPLICANT
(Use several sheets if necessary)

ATTY. DOCKET NO.
MI22-1939SERIAL NO.
10/072,417APPLICANT
Joseph M. BrandFILING DATE
February 5, 2002GROUP
2826

U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	5,776,801	07/07/98	Carper et al.			
	AB						
	AC						
	AD						
	AE						
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	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/072,417
Filing Date February 5, 2002
Inventor Joseph M. Brand
Assignee Micron Technology, Inc.
Group Art Unit 2826
Examiner Pershelle L. Greene
Attorney's Docket No. MI22-1939
Title: Integrated Circuit Device

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Citation of the referenced art is respectfully requested.

Respectfully submitted,

Dated: 1-21-03

By: D. Brent Kenady
D. Brent Kenady
Reg. No. 40,045

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AUG 21 2003
TC 2800 MAIL ROOM



AUG 14 2003

Form PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEDOCKET NO.
SM2-1939SERIAL NO.
10,072,417LIST OF ART CITED BY APPLICANT
(Use several sheets if necessary)APPLICANT
Joseph M. BrandFILING DATE
February 5, 2002GROUP
2826

U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	5,097,317	03/17/92	Fujimoto et al.	357	72	
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						

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